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<u>L18</u>	field\$1 near7 data near2 type near12 format near7 vector	9	<u>L18</u>
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<u>L14</u>	L13 near15 (fetch\$5 or prefetch\$5)	35	<u>L14</u>
<u>L13</u>	(set) near1 address near1 registers	1700	<u>L13</u>
<u>L12</u>	(set or group or plurality) near4 address near1 registers	9548	<u>L12</u>
<u>L11</u>	(set or group or plurality) near5 address\$2 near5 registers	17473	<u>L11</u>

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IEEE STD	IEEE Standard

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- ☐ 1. **A high speed microprocessor core for the embedded applications-PKURS001**  
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- ☐ 2. **MARS-a RISC-based architecture for LISP**  
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